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1 Concurrent meld

G. E. Kaiser
September 1988 Proceedings of the 1988 ACM SIGPLAN workshop on Object-based concurrent programming
Publisher: ACM

Full text available: pdf(293.04 KB) Additional Information: full citation, abstract, references, cited by, index terms

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Our original goal was to design a programming language for writing software engineering environments. The most important requirements were reusability and the ability to integrate separately developed tools [1]. Our scope was later expanded to general ...

2 Concurrent meld

G. E. Kaiser
April 1989 ACM SIGPLAN Notices, Volume 24 Issue 4
Publisher: ACM

Full text available: pdf(293.04 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 3, Citation Count: 1

Our original goal was to design a programming language for writing software engineering environments. The most important requirements were reusability and the ability to integrate separately developed tools [1]. Our scope was later expanded to general ...

3 Fast exploration of bus-based communication architectures at the CCATB abstraction

Sudeep Pasricha, Nikil Dutt, Mohamed Ben-Romdhane
February 2008 ACM Transactions on Embedded Computing Systems (TECS), Volume 7 Issue 2
Publisher: ACM

Full text available: pdf(1.14 MB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 3, Citation Count: 0

Currently, system-on-chip (SoC) designs are becoming increasingly complex, with more and more components being integrated into a single SoC design. Communication between these components is increasingly dominating critical system paths and frequently ...

Keywords: System-on-chip, communication architecture, on-chip bus, performance exploration, transaction-level modeling

4 On permissions, inheritance and role hierarchies

Jason Crampton
October 2003 CCS '03: Proceedings of the 10th ACM conference on Computer and communications security
Publisher: ACM

Full text available: pdf(198.61 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 116, Citation Count: 6

Role-based access control and role hierarchies have generated considerable research activity in recent years.In many role-based models the role hierarchy partially determines which roles and permissions are available to users via various inheritance ...

Keywords: inheritance, mandatory access control, permissions, role-based access control

5 Extending the transaction level modeling approach for fast communication architecture exploration

Sudeep Pasricha, Nikil Dutt, Mohamed Ben-Romdhane
June 2004 DAC '04: Proceedings of the 41st annual conference on Design automation
Publisher: ACM

Full text available: pdf(409.83 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 44, Citation Count: 16

System-on-Chip (SoC) designs are increasingly becoming more complex. Efficient on-chip communication architectures are critical for achieving desired performance in these systems. System designers typically use Bus Cycle Accurate (BCA) models written ...

Keywords: AMBA, bus cycle accurate modeling, communication architecture exploration, shared bus architectures, transaction level modeling

6 Memory binding for performance optimization of control-flow intensive behaviors

Kamal S. Khouri, Ganesh Lakshminarayana, Niraj K. Jha
November 1999 ICCAD '99: Proceedings of the 1999 IEEE/ACM international conference on Computer-aided design
Publisher: IEEE Press

Full text available: pdf(164.71 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 12, Citation Count: 3

This paper presents a memory binding algorithm for behaviors that are characterized by the presence of conditionals and deeply-nested loops that access memory extensively through arrays. Unlike previous works, this algorithm examines the effects of branch ...

7 DejaView: a personal virtual computer recorder

Oren Laadan, Ricardo A. Baratto, Dan B. Phung, Shaya Potter, Jason Nieh
October 2007 SOSP '07: Proceedings of twenty-first ACM SIGOPS symposium on Operating systems principles
Publisher: ACM
Full text available: pdf(534.51 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 129, Citation Count: 0

As users interact with the world and their peers through their computers, it is becoming important to archive and later search the information that they have *viewed*. We present DejaView, a personal virtual computer recorder that provides a complete ...

Keywords: desktop search, virtualization

8 An adaptive data replication algorithm

Ouri Wolfson, Sushil Jajodia, Yixiu Huang
June 1997 ACM Transactions on Database Systems (TODS), Volume 22 Issue 2
Publisher: ACM
Full text available: pdf(911.08 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 21, Downloads (12 Months): 215, Citation Count: 54

This article addresses the performance of distributed database systems. Specifically, we present an algorithm for dynamic replication of an object in distributed systems. The algorithm is adaptive in the sence that it changes the replication scheme of ...

Keywords: computer networks, dynamic data allocation, file allocation, replicated data

9 Synthesis of concurrent programs for an atomic read/write model of computation

Paul C. Attie, E. Allen Emerson
March 2001 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 23 Issue 2
Publisher: ACM
Full text available: pdf(614.52 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 55, Citation Count: 2

Methods for mechanically synthesizing concurrent programs for temporal logic specifications have been proposed by Emerson and Clarke and by Manna and Wolper. An important advantage of these synthesis methods is that they obviate the need to manually ...

Keywords: atomic registers, concurrent programs, program synthesis, specification, temporal logic

10 GPGPU: general purpose computation on graphics hardware

David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn
August 2004 SIGGRAPH '04: ACM SIGGRAPH 2004 Course Notes
Publisher: ACM

Full text available: pdf(63.03 MB) Additional Information: full citation, abstract, cited by

Bibliometrics: Downloads (6 Weeks): 71, Downloads (12 Months): 1505, Citation Count: 3

The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex ...

11 The collective memory of amnesic processes

Rachid Guerraoui, Ron R. Levy, Bastian Pochon, Jim Pugh
March 2008 ACM Transactions on Algorithms (TALG), Volume 4 Issue 1
Publisher: ACM

Full text available: pdf(560.10 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 0, Citation Count: 0

This article considers the problem of robustly emulating a shared atomic memory over a distributed message-passing system where processes can fail by crashing and possibly recover. We revisit the notion of atomicity in the crash-recovery context and ...

Keywords: Atomic registers, crash recovery, log complexity, shared-memory emulation

12 Adaptive, fine-grained sharing in a client-server OODBMS: a callback-based approach

Markos Zaharioudakis, Michael J. Carey, Michael J. Franklin
December 1997 ACM Transactions on Database Systems (TODS), Volume 22 Issue 4
Publisher: ACM

Full text available: pdf(441.80 KB) Additional Information: full citation, abstract, references, cited by, index terms, review

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 71, Citation Count: 5

For reasons of simplicity and communication efficiency, a number of existing object-oriented database management systems are based on page server architectures; data pages are their minimum unit of transfer and client caching. Despite their efficiency, ...

Keywords: cache coherency, cache consistency, client-server databased, fine-grained sharing, object-oriented databases, performance analysis

13 Reducing register ports using delayed write-back queues and operand pre-fetch

Nam Sung Kim, Trevor Mudge
June 2003 ICS '03: Proceedings of the 17th annual international conference on Supercomputing
Publisher: ACM

Full text available: pdf(381.44 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 39, Citation Count: 12

In high-performance wide-issue microprocessors the access time, energy and area of the register file are often critical to overall performance. This is because these pararmeters grow superlinearly as read and write ports are added to support wide-issue. ...

Keywords: instruction level parallelism, low power, out-of-order processor, register file, write queue

14 Charles W. Bachman interview: September 25-26, 2004; Tucson, Arizona

Thomas Haigh
January 2006 ACM Oral History interviews
Publisher: ACM
Full text available: pdf(974.87 KB) Additional Information: full citation, abstract, index terms

Bibliometrics: Downloads (6 Weeks): 24, Downloads (12 Months): 432, Citation Count: 0

Charles W. Bachman reviews his career. Born during 1924 in Kansas, Bachman attended high school in East Lansing, Michigan before joining the Army Anti Aircraft Artillery Corp, with which he spent two years in the Southwest Pacific Theater, during ...

15 Fast and secure distributed read-only file system

Kevin Fu, M. Frans Kaashoek, David Mazières
February 2002 ACM Transactions on Computer Systems (TOCS), Volume 20 Issue 1
Publisher: ACM
Full text available: pdf(317.54 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 237, Citation Count: 11

Internet users increasingly rely on publicly available data for everything from software installation to investment decisions. Unfortunately, the vast majority of public content on the Internet comes with no integrity or authenticity guarantees. This ...

Keywords: File systems, read-only, security

16 A uniform type structure for secure information flow

Kohei Honda, Nobuko Yoshida
October 2007 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 29 Issue 6
Publisher: ACM
Full text available: pdf(1.42 MB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 151, Citation Count: 0

The π-calculus, a calculus of mobile processes, can compositionally represent dynamics of major programming constructs by decomposing them into name passing. The present work reports our experience in using a linear/affine typed π-calculus for ...

Keywords: The π-calculus, secure information flow, type-based program analysis, typing system

17 Data and memory optimization techniques for embedded systems

P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A. Vandercappelle, P. G. Kjeldsberg
April 2001 ACM Transactions on Design Automation of Electronic Systems (TODAES), Volume 6 Issue 2
Publisher: ACM
Full text available: pdf(339.91 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 38, Downloads (12 Months): 520, Citation Count: 52

We present a survey of the state-of-the-art techniques used in performing data and memory-related optimizations in embedded systems. The optimizations are targeted directly or indirectly at the memory subsystem, and impact one or more out of three important ...

Keywords: DRAM, SRAM, address generation, allocation, architecture exploration, code transformation, data cache, data optimization, high-level synthesis, memory architecture customization, memory power dissipation, register file, size estimation, survey

18 Static scheduling of multi-domain memories for functional verification

Murali Kudlugi, Charles Selvidge, Russell Tessier

November 2001 ICCAD '01: Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design

Publisher: IEEE Press

Full text available: pdf(113.07 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 19, Citation Count: 0

Over the past decade both the quantity and complexity of available on-chip memory resources have increased dramatically. In order to ensure accurate ASIC behavior, both logic functions and memory resources must be successfully verified before fabrication. ...

19 Design and evaluation of a conit-based continuous consistency model for replicated services

Haifeng Yu, Amin Vahdat

August 2002 ACM Transactions on Computer Systems (TOCS), Volume 20 Issue 3

Publisher: ACM

Full text available: pdf(406.85 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 28, Downloads (12 Months): 141, Citation Count: 7

The tradeoffs between consistency, performance, and availability are well understood. Traditionally, however, designers of replicated systems have been forced to choose from either strong consistency guarantees or none at all. This paper explores the ...

Keywords: Conit, consistency model, continuous consistency, network services, relaxed consistency, replication

20 The Google file system

Sanjay Ghemawat, Howard Gobioff, Shun-Tak Leung

October 2003 SOSP '03: Proceedings of the nineteenth ACM symposium on Operating systems principles

Publisher: ACM

Full text available: pdf(275.54 KB) Additional Information: full citation, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 208, Downloads (12 Months): 1795, Citation Count: 52

Keywords: clustered storage, data storage, fault tolerance, scalability

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